#### RESEARCH



# The experimental approach into the influence of external inductance on the discharge characteristic of HiPIMS

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#### Abstract

The main objective of the current paper is to describe the effect of external inductance (EI) on the current discharge waveforms of HiPIMS at different pulse-on time ( $P_{on}$ ) and its relation with static deposition rate and topographical properties of deposited titanium thin films, which is investigated by scanning electron microscope and atomic force microscope. It has shown that the higher the EI, independent of the  $P_{on}$ , the higher the peak power is. The delay time also extensively increases when an EI is implemented into the circuit. However, the rise time does not have a linear dependency with the EI and its behavior changes to some extent at different  $P_{on}$ . By increasing the EI from zero to 30 mH at  $P_{on} = 60 \mu$ s, the peak power subsequently rises from 11 to 32 kW at constant time-average power. Meanwhile, the deposition rate decreases from 8.5 to 1.5 nm/min, which is mainly attributed to the metal ions return to the target surface and nonlinear dependency of sputtering yield with applied voltage. It was also revealed that the higher peak power has no special effect on the surface roughness of titanium thin films deposited by HiPIMS.

Keywords Deposition rate · External inductance · HiPIMS · Surface roughness

# Introduction

Magnetron sputtering (MS) is one of the most common methods to fabricate a variety of thin films with specific properties appropriate for industrial applications [1-7]. Unbalanced magnetron sputtering (UMS) and closed field unbalanced magnetron sputtering (CFUMS) are a step forward of classical MS in which they provide higher deposition rate, denser and more spacious plasma expanded to the substrate while the quality of thin films is also enhanced [6, 8, 9]. The enhancement of thin films quality is strongly connected to the ratio of the ion to atom transported into the substrate [10, 11], in which they not only have enough kinetic energy to be located on the thermodynamically lower energy place but also they can kick off loosely bonded atoms from the substrate surface, providing the negative bias voltage is applied. Although an advantage of such mentioned methods is to lack micron-size droplets [12–14], which is prevalent in cathodic arc physical vapor

Hamid Ghomi h-gmdashty@sbu.ac.ir deposition (CAPVD), the deposition rate and the amount of ionization rate are inferior to that of CAPVD method. Much research has been carried out to improve the ionization rate of plasma and the deposition rate of coatings in the MS system [15–20]. High-power impulse magnetron sputtering (HIPIMS) has recently attracted much interest owing to its capability to deposit droplet-free coatings with a high degree of ionization in the plasma that is desirable for some films [21, 22]. The high ionization is of interest because it gives rise to dense coatings as well as good adhesion on the substrate [23]. Therefore, many researchers worldwide have been motivated to fully exploit the features of this method [24–28].

The current waveforms in HiPIMS are a fingerprint of the system, indicating the ionization rate of Ar, gas sputtering, self-sputtering, metal ionization, waves, and instabilities, and it has been covered in the literature under different conditions [29–32]. For instance, in Mclain's work [33] who studied the deposition rate of HiPIMS with respect to the arrangement of magnets, the shape of the current waveforms of HiPIMS, correspondingly with the plasma confinement nearby the cathode, has altered from runaway to plateau, resulting in distinct deposition rate. In another work by Zuo et al. [34], the current waveforms for a variety of target

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materials are discussed in which all have some humplike shape with distinct peak current that is related to the secondary electron emission yield. In another paper [35], the target current at different reactive gas pressures is considered, demonstrating the higher the gas pressure, the higher the peak current reaches. The correlation between the peak current in HiPIMS with the microstructure of coatings was explored by Alami et al. [36]; when the high peak current is achieved, the density and surface roughness of deposited films changed, whereas the deposition rate tremendously reduced. Additionally, the crystallinity of the films decreased so that no XRD peaks were finally detected. They reasoned that the high ionization rate in higher peak current leads to more ions reaching to the substrate, changing the films morphology. This is also reported by [36] that the ion-toatom ratio near the substrate increased around 3 times when the peak current changed from 113 to 185 A. The current waveforms for different target materials showed that the peak current value and the current plateau are corresponding with the rarefaction (or sputter wind) and ionization rate of metals, respectively [37]. The comparison of the current waveforms of HiPIMS and 1.0 A-DC-superimposed HiPIMS is indicative of the higher peak current in HiPIMS while the slope of the current exceeds for DC-HiPIMS [38]. Revel et al. [39] have theoretically and experimentally investigated the effect of an external circuit with a given resistance on the current waveforms of HiPIMS. They observed that the lower external series resistor leads to the higher current plateau.

Despite the many studies on the features of current waveforms, the effect of an EI on the properties of current waveforms of HiPIMS has not been explored. Therefore, in this work, the influence of an EI on the current waveforms and static deposition rate and topographical properties of deposited titanium is addressed.

#### Experimental procedure

The experiments were carried out in a home-made vacuum deposition system that is described elsewhere [40]. The HiPIMS system was also designed and fabricated in our laboratory. The power supply of HiPIMS has capabilities to produce pulses with  $P_{\rm on}$  and frequency in the range of 20–500 µs and 100–1500 Hz, respectively. A DC power supply (1–1000 V and 1 A) was used to support the HiPIMS power, in which the maximum target voltage and current could increase up to 1000 V and 100 A, respectively.

The electric scheme of HiPIMS system is shown in Fig. 1. A DC power supply charges the Cs capacitor bank (storage capacitor). The energy stored in the capacitor bank is dissipated into the plasma using the ultrafast switch. The pulse width modulator (PWM) determines on and off the timing of switch in pulses of well-defined width and frequency. A series resistor R3 of 3 ohms is employed to limit the discharge current. In order to study the effect of EI on discharge characteristics of HiPIMS discharge, a variable inductance in series with cathode is implemented as L1 in Fig. 1 in which its value alters from 0 to 70 mH.

During experiments, the average power was set to 250 W by regulating the value of voltage. The effect of the variable inductance on HiPIMS discharge characteristics was studied at a frequency of 500 Hz and five values of  $P_{\rm on}$  in the range of 60–300 µs. The amount of EI had different values of 0, 5, 10, 20, 30, 40, 50, 60, and 70 mH. The other experimental parameters such as duty cycle, working pressure, gas flow ratio are rendered in Table 1.

The voltage was measured at two points: point A, which includes the summation of inductor and magnetron sputtering and point B, which includes only magnetron sputtering. One should note that the average power is calculated based



Fig. 1 The electric circuit of the HiPIMS system

 Table 1
 The parameters for titanium thin films deposition and evolution of current waveforms

Parameters	Ti deposition	Current waveforms
Target to substrate distance	9 cm	_
Bias voltage	– 100 V	-
Base pressure	$5 \times 10^{-5}$ Torr	-
Average power	250 W	250 W
Pressure	5 mTorr	5 mTorr
Substrate temperature	250 °C	_
Frequency	500 Hz	500 Hz
Duty cycle (%)	3	3, 5, 7.5, 10, 15

on the voltage of point A, assuming the energy loss in the inductor is negligible. The deposition parameters of titanium thin films and those relating to extracting evolution of current waveforms data are also listed in Table 1. The topography of deposited thin films was evaluated by atomic force microscopy (AFM) in contact mode (park scientific instrument-CP auto probe). Field emission scanning electron microscope (FESEM) was also used to measure the film thickness so that the static deposition rate [41] is calculated.

## **Results and discussion**

## **Current waveforms**

In this part, the current waveforms at different EI and  $P_{on}$  are discussed (Fig. 2). In Fig. 2a, the current evolution for EI equal to 0, 5, 30, and 60 mH at 60 µs is plotted. When the EI is zero, as it is a normal HiPIMS circuit, the current after a short time (delay time) goes up for about 10 µs and it reaches to steady state, named current plateau. Finally, it sharply drops to a little negative and stays close to zero until it turns on again, which is marked as pulse-off time. When an EI is implemented into the external circuit, the current waveform is extremely changed. As seen in the beginning, the current shows a transient peak and then it remains constant and positive near to zero that can be labeled as the delay time. The delay time, here, is extended due to the



Fig. 2 The current waveforms for different EI where  $P_{on}$  is a 60 µs, b 100 µs, c 150 µs, and d 200 µs

inductance phenomena. After around 25 µs up to 40 µs, which is dependent on the value of EI, the current starts to increase more or less sharply to a specific value of current around 50 A (Fig. 2a) and then falls too sharply to below zero when the pulse ends and it stays negative for some microseconds. The description of other current waveforms at different  $P_{on}$  is very similar to that of  $P_{on} = 60 \ \mu s$ . However, in Fig. 2b, when the EI is equal to 5 mH, the current after a delay time goes to plateau phase while for the higher EI, not only the delay time lasts longer, but also the plateau disappeared and it seems the current is somehow transient. In other words, when the  $P_{on}$  is quite short (60 µs), or the EI is relatively high, the current plateau is not observed and the peak current is at the end of the pulse, implying the current is transient. In Fig. 2c, when the  $P_{\rm on}$  is 150 µs, the transient current is only observed for EI equal to 60 mH and for lower EI, the current plateau is reached. When the  $P_{on}$  increases to 200 µs (Fig. 2d), even at high EI, the current plateau is observed that is originated from the lower applied voltage so that the time-average power remains constant.

As it was stated in the experimental section, the voltage is measured at points A and B (Fig. 1). The voltage at point A for  $P_{on} = 60 \,\mu s$  is represented in Fig. 3a. As seen, the voltage shape is squared, however, after the delay time when the current starts to increase sharply (Fig. 2a), the voltage drops and it continues decreasing as long as the current rises. It is such a normal phenomenon that the value of voltage decreases when the sputtering turns on, i.e., the impedance of the system decreases when the current begins to rise and the decline of applied voltage occurs that is also observed here [42]. Figure 3b depicts the peak voltage in terms of EI at a variety of  $P_{on}$ . The general trend for applied voltage is easily perceived so that the higher the EI, the higher the value of applied voltage (point A) is. In addition, when the  $P_{\rm on}$  increases, the applied voltage reduces. One should note that the time-average power in all data is constant and equal

50

800

(a)



600 400 I П III IV Discharge voltage (V) Discharge current (A) 200 30 0 20 -200 Point B -400 -600 -800 . Point A -1000 0 20 40 60 80 Time (µs) 1000 **(b)** 50 500 III IV Discharge current (A) Discharge voltage (V) 30 ( Point B -500 -1000 Point A -10 20 40 60 0 80 Time (µs)

**Fig. 3** a The applied voltage (point A in Fig. 1) shape for  $P_{on} = 60 \ \mu s$ , **b** and the peak voltage for different EI and  $P_{on}$ 

**Fig. 4** The current waveforms and the voltage (A and B in Fig. 1) at  $P_{on} = 60 \ \mu s$  for **a** EI = 5 mH and **b** EI = 30 mH

to 250 W; the voltage is regulated in a way to reach the given power. It is the reason why the longer duration of  $P_{\rm on}$ , results in lower current and voltage.

Since the voltage at point B (Fig. 1) is essential for describing the current waveforms, in particular, the beginning transient peak and extended delay time, it is measured and depicted in Fig. 4. The shape of this voltage is different from the voltage at point A owing to the EI effect. That is here discussed in detail. For the sake of simplicity, the voltage B shape is divided into five different phase at  $P_{\rm on} = 60 \,\mu s$ with EI = 5 mH (Fig. 4a). Part I: At the very beginning, since there is no current flowing through the inductor, the applied voltage at point B suddenly drops to such a value similar to voltage A. Part II: As soon as the current rises or the discharge takes place, the inductor reacts to the current flow and based on the Lens law, decreases the voltage B. Since the voltage drops at point B, the discharge becomes terminated. Part III: The current decays and the value of voltage start to increase gently. Part IV: The increasing behavior of voltage causes the current to increase slowly. Part V: Finally, after a delay time around 30 µs, the inductor is saturated and the current rises as there was no inductance in the circuit that is proven by equality of voltages A and B. It is worth mentioning that this shape of voltage B is very similar to other  $P_{on}$ at EI = 5 mH. However, when the EI is increased to 30 mH (Fig. 4b), the voltage shape somehow varies. One difference is about part II, in which the voltage drop is sharper and more than the latter one (EI = 5 mH).

According to the current waveforms in Fig. 2, the peak current, the rise time, and the delay time for different  $P_{on}$ and EI are shown in Fig. 5. Besides, the peak power is also calculated and is shown in this figure. Figure 5a presents the peak current with respect to EI for different  $P_{on}$ . The highest peak current belongs to the  $P_{on} = 60 \ \mu s$  and as  $P_{on}$ increases, the peak current declines. One should note that the increase of EI leads to a higher peak current that is also consistent with peak power. However, for the  $P_{on} = 60 \ \mu s$ , using EI = 5 mH increases the peak current and the peak power around two times in compression with no EI. When EI increased to 10 mH, the increase of the peak current is not noticeable and the peak current increases very gently with respect to the EI. However, the increase of peak



**Fig. 5** The effect of EI at a variety of  $P_{on}$  on the **a** peak current, **b** peak power, **c** rise time, **d** delay time

power for the  $P_{\rm on} = 60 \ \mu s$  is more considerable than the respective peak current. For the other  $P_{\rm on}$ , the increasing trend of peak current and peak power with EI is very similar.

The rise time graph (Fig. 5c) exhibits that the longer the  $P_{\rm on}$ , the longer the rise time is. However, the variation of rising time with EI is not well defined. Figure 5d shows that the increase of EI and  $P_{\rm on}$  leads to longer delay time. When the EI is stronger, it absorbs more energy to allow the current passes through and consequently, the delay time becomes longer. As the applied voltage is lower for the higher  $P_{\rm on}$ , the time for core saturation of inductor is longer and the delay time is extended.

## Thin films properties

In this section, the deposition rate and surface roughness of Ti deposited layers are discussed.

### **Deposition rate**

The deposition rate referred as static deposition rate [41] is such an important industrial parameter that is calculated here as the thickness divided by the deposition time, assuming the density of films is equal to bulk. The SEM cross section image of layers is shown in Fig. 6. It is obvious that the thickness of layers, equivalently the deposition rate, decreases as the higher EI is set on the circuit (Fig. 7), which is derived on the higher peak current (power and higher applied voltage). Note that although the peak current can be used as a factor to compare different HiPIMS system, the peak power is a better choice since it is indicative for energy that is conserved in the system. As the peak power increases, the temperature of electrons also rises, leading to the higher ionization rate and the more likely; the multiply charged metal ions are formed. The multiply charged ions are less likely to escape the negative target potential and they probably will return to the target, causing self-sputtering, and as a result, it may lower the deposition rate. As stated by Anders [41], the deposition rate at the substrate is proportional to the voltage squared. If the average power is constant, increasing the voltage results in lower deposition rate due to yield effects of sputtering species. Hence, the ratio of power-normalized rate is formulated as  $\rho = k^{-0.5}$ , where k is the ratio of two selected voltages and  $\rho$  is the factor that the deposition rate is reduced. Here, for EI equals zero and 30 mH, the voltage is - 500 and - 800 V, respectively. This







Fig. 7 The deposition rate of Ti thin films on silicon at  $P_{\rm on}$ =60 µs with respect to EI

gives rise to  $\rho = \left(\frac{800}{500}\right)^{-0.5} = 0.79$ , i.e., a decrease in deposition rate around 21% is related to yield effects.

#### Surface roughness

The topography of the surface is of high prominence in most application. There are many roughness parameters to describe the surface features. Here, the two most common parameter including average roughness (Sa) and root mean square (Sq) is used to evaluate the surface. The three-dimensional surface images, as well as the histogram of four samples deposited with  $P_{\rm on} = 60 \ \mu s$  at different EI, are shown in Fig. 8. As seen, no meaningful changes in the topography of the surface are traced. The Sa and Sq are also plotted with respect to EI, showing no dependency on the EI value or the respective peak current (Fig. 9). The roughness of the surface in sputtering is highly dependent on the energy of atoms and ions reaching the substrate surface. Regardless of using an EI in the circuit, the plasma in HiPIMS consists of a high ratio of ions to atoms comparing to DC-magnetron sputtering. Consequently, the ratio of ions to neutral atoms in the vicinity of the substrate surface is considerable. Since a negative voltage bias is applied to the substrate surface, the ions can be accelerated into the surface with enough kinetic energy to fill the valley of the surface and make the shadowing effect negligible. Since the HiPIMS method, regardless of the value of peak power, can produce a significant amount of ions, it seems logical that the value of Sa and Sq shows no noticeable variation with respect to EI and peak power.

## Conclusion

This study aimed to uncover the effect of an external inductance (EI) in the HiPIMS circuit on the evolution of current waveforms and the respective topographical and static deposition rate of titanium thin films. The obtained results are provided here:



Fig. 8 The topography and histogram of deposited titanium thin films with  $P_{on} = 60 \ \mu s$  at a EI = 0, b EI = 2.5 mH, c EI = 5 mH, and d EI = 30 mH



**Fig. 9** The arithmetic average roughness (Sa) and root mean square  $(S_{\rm q})$  of deposited titanium thin films with  $P_{\rm on} = 60 \ \mu s$  at different EI

- (a) When the higher EI was implemented into the HiPIMS circuit or the lower pulse-on time  $(P_{on})$  was set, the peak power and the peak current increased. Note that the time-average power remained constant in all experiments and that is the reason why the peak current increased when the duration of pulse decreased. Furthermore, when the higher EI is used, the more extended delay time appears, and it means that the real on time of the pulse decreased, leading to a higher peak current and power.
- (b) When the value of EI increased from 0 to 30 mH, the peak power went up from 11 to 32 kW, whereas the deposition rate decreases from 8.5 to 1.5 nm/min which it stems from the higher return of metal ions into the cathode and the nonlinear dependency of sputtering yield of target with the voltage.
- (c) The variation of EI did not influence the surface topography that could be related to the high amount of ionization in HiPIMS regardless of EI; i.e., the large ratio of ion to atom in HiPIMS leads to the ions with sufficient energy transported to the substrate surface and overcoming the possible shadowing effect. Hence, the more increasing of ion to atom ratio does not change relatively the surface roughness.

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