

Razieh Farazkish

Associate Professor

DoB: 1983/5/3

Marital Status: Married

- ✓ r.farazkish@gmail.com
- (+98)9129379028
- **1**+98-21-22849922
- Unit 9, No. 13, Negarestan 10th Alley, Pasdaran St., Tehran, Iran



Skills

DL MSCE Q

QCA Designer Simulator

Verilog, VHDL Programming

java programming language

c, c++, matlab

data analysis and modeling



Languages

English

Reading Level

Writing Level

Speaking Level

Listening Level



Reading Level

Writing Level

Speaking Level

Listening Level



Reading Level

Writing Level

Speaking Level

Listening Level





About

Associate Professor and Faculty Member, Director of Computer Engineering,
Supervision of more than 100 students in master's and doctoral degrees, with
numerous articles in the field of secure systems, fault-tolerant systems, quantum
computing, nanorobotics and nanoelectronics.



Education

Doctorate of computer engineering

Branch: Computer Systems Architecture
Institute/University: Science and Research Branch
of IAU

2008 - 2011

GPA: 19 (3.8/4)

Bachelor of computer engineering

Branch: hardware

Institute/University: Central Tehran Branch of IAU

2002 - 2006

GPA: 17.00 (3.4/4)

Master of computer engineering

Branch: Computer Systems Architecture
Institute/University: Science and Research Branch
of IAU

2006 - 2008

GPA: 18.90 (3.78/4)



Work Experiences

Associated Professor

University: IAU

From January 2006

Researcher

Educational Institutions: IAU

From January 2002



Researches

Design of Fault-tolerant systems

Publisher: South Tehran Branch of IAU

Date: March 2018



in ?originalsubdomain=ir

A new quantum-dot cellular automata full-adder

Publisher: Elsevier, Microelectronics Journal

Date: December 2010

Five-input majority gate, a new device for quantum-dot cellular automata

Publisher: American Scientific Publishers, Journal of Computational and Theoretical Nanoscience

Date: August 2010

Design and characterization of a new fault-tolerant full-adder for quantum-dot cellular automata

Publisher: Elsevier, Microprocessors and Microsystems

Date: August 2015

New quantum dot cellular automata cell arrangements

Publisher: American Scientific Publishers, Journal of Computational and Theoretical Nanoscience

Date: April 2013

A new quantum-dot cellular automata fault-tolerant full-adder

Publisher: Springer US, Journal of Computational Electronics

Date: June 2016

A new quantum-dot cellular automata fault-tolerant five-input majority gate

Publisher: Springer Netherlands, Journal of nanoparticle research

Date: February 2014

New method for decreasing the number of quantum dot cells in QCA circuits

Publisher: International Digital Organization for Scientific Information

Date: January 2008

Novel design for quantum dots cellular automata to obtain fault-tolerant majority gate

Publisher: Hindawi, Journal of Nanotechnology

Date: January 2012

New efficient five-input majority gate for quantum-dot cellular automata

Publisher: Springer Netherlands, Journal of Nanoparticle Research

Date: November 2012

FCCTF: Fairness Congestion Control for a distrustful wireless sensor network using Fuzzy logic

Publisher: IEEE

Date: September 2010

Novel efficient fault-tolerant full-adder for quantum-dot cellular automata

Publisher: Islamic Azad University-Tonekabon Branch

Date: February 2018

Robust and reliable design of bio-nanorobotic systems

Publisher: Springer Berlin Heidelberg, Microsystem Technologies

Date: April 2019

A new method for routing optimization in vehicular ad hoc networks (VANETs)

Publisher: Islamic Azad University, South Tehran Branch

Date: March 2019

Fault-Tolerant Techniques for Quantum-dot Cellular Automata Circuits and Systems

Publisher: Islamic Azad University, South Tehran Branch

Date: March 2020

New Fault-Tolerant Majority Gate for Quantum Dots Cellular Automata

Publisher: Islamic Azad University, South Tehran Branch

Date: March 2017

Reliability modeling in bio-nano robots

Publisher: Journal of Iranian Association of Electrical and Electronics Engineers

Date: September 2020

A Novel Method Based on Support Vector Machines to Classify Bank Transactions

Publisher: Islamic Azad University, South Tehran Branch

Date: June 2019



Distinguished student and the first rank in PHD degree.

Date: February 2011

Distinguished student and the first rank in M. Sc. degree.

Date: February 2008